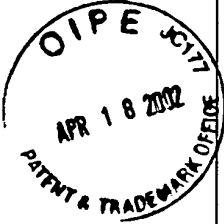


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Jack H. Yuan, Eliyahou Harari, Yupin K. Fong and George Samachisa  
Assignee: SanDisk Corporation  
Title: Scalable Self-Aligned Dual floating Gate Memory Cell Array And Methods Of Forming The Array  
Serial No.: 09/925,102 Filing Date: August 8, 2001  
Examiner: Group Art Unit: 2185  
Docket No.: M-11822 US

San Francisco, California  
April 18, 2002

BOX MISSING PARTS  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

PETITION FOR EXTENSION OF TIME

Dear Sir:

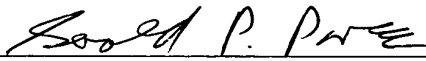
Applicants respectfully petition for an extension of time within which to respond to the September 28, 2001, outstanding Notice to File Missing Parts -- Filing Date Granted, such extension allowing the undersigned until April 28, 2002 to respond.

Please charge the amount of \$1,960.00 as set forth in the enclosed transmittal letter.

EXPRESS MAIL  
LABEL NO.:

EV029355994US

Respectfully submitted,

  
Gerald P. Parsons  
Reg. No. 24,486

April 18, 2002  
Date

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